

# Course Outline (F2021)

# ELE724: CMOS Mixed-Mode Circuits and Systems

| Instructor(s)                          | Dr. Fei Yuan [Coordinator]<br>Office: ENG433<br>Phone: (416) 979-5000 x 556100<br>Email: fyuan@ryerson.ca<br>Office Hours: Tuesday, 2-3 pm  |  |  |
|--|---|--|--|
| Calendar<br>Description                | This course deals with the design of CMOS mixed-mode circuits and systems. Key topics include switching noise, analog and digital grounding, electrostatic discharge protection, clock and power distribution, fundamentals of analog-to-digital converters (ADCs), Nyquist ADCs including flash, pipelined and successive approximation ADCs, introduction to switched-capacitor networks, over-sampling ADCs, dynamic element matching, time-mode ADCs, and decimation filters.   |  |  |
| Prerequisites                          | ELE 639   |  |  |
| Antirequisites                         | None  |  |  |
| Corerequisites                         | None  |  |  |
| Compulsory<br>Text(s):                 | <ol> <li>No single text can serve as the text book of this advanced course on CMOS mixed-mode<br/>circuits and systems.</li> <li>ELE 724 Lecture Notes from Dr. Fei Yuan (available from D2L).</li> <li>Laboratory manual: ELE 724 Laboratory Manual (available from D2L).</li> <li>Post-lecture lecture notes after each lecture will be made available to students after lectures.</li> </ol>   |  |  |
| Reference<br>Text(s):                  | <ol> <li>B. Razavi, Design of Analog CMOS Integrated Circuits, 2nd ed. McGraw-Hill, 2017</li> <li>T. Carusone, D. John, and K. Martin, Analog Integrated Circuits Design, 2nd edition, John Wiley and Sons, 2011.</li> <li>R. Schreier and G. Temes, Understanding Delta-Sigma Data Converters, John Wiley and Sons, 2005.</li> <li>F. Yuan, CMOS Time-Mode Circuits : Principles and Applications, CRC Press, 2015.</li> </ol>   |  |  |
| Learning<br>Objectives<br>(Indicators) | <ul> <li>At the end of this course, the successful student will be able to:</li> <li>1. Improve their capabilities of using the knowledge of CMOS integrated circuits and computer-aided design tools to design CMOS mixed-mode integrated circuits. (4b)</li> <li>2. Utilize computer-aided design tools for analysis and design of integrated circuit to iteratively improve the performance of CMOS mixed-mode integrated circuits to meet design specifications. (4c)</li> <li>3. Proficiency in use of computer-aided design tools from Cadence Design Systems for analysis and design of integrated circuit to design and analyze CMOS mixed - mode integrated circuits and systems. (5a)</li> <li>4. Write professionally prepared laboratory reports. Laboratory reports are evaluated on their correctness, completeness, English, and quality of graphics (7a)</li> </ul> |  |  |

|                                    | <ul> <li>5. Write professionally prepared laboratory reports. Laboratory reports are evaluated on their correctness, completeness, English, and quality of graphics. (7c)</li> <li>NOTE:Numbers in parentheses refer to the graduate attributes required by the Canadian Engineering Accreditation Board (CEAB).</li> </ul>  |   |       |  |
|------------------------------------|--|---|-------|--|
| Course<br>Organization             | 3.0 hours of lecture per week for 13 weeks<br>2.0 hours of lab per week for 12 weeks<br>0.0 hours of tutorial per week for 12 weeks  |   |       |  |
| Teaching<br>Assistants             | Parth Par  | Parth Parekh (email: parth.parekh@ryerson.ca) |       |  |
|                                    |  | Theory  |       |  |
|                                    |  | Midterm Exam                                  | 30 %  |  |
|                                    |  | Final Exam                                    | 30 %  |  |
|                                    |  | Laboratory                                    |       |  |
| Course                             |  | Lab Projects                                  | 40 %  |  |
| Course<br>Evaluation               |  | TOTAL:  | 100 % |  |
|                                    | <b>Note:</b> In order for a student to pass a course, a minimum overall course mark of 50% must be obtained. In addition, for courses that have both <b>"Theory and Laboratory"</b> components, the student must pass the Laboratory and Theory portions separately by achieving a minimum of 50% in the combined Laboratory components and 50% in the combined Theory components. Please refer to the <b>"Course Evaluation"</b> section above for details on the Theory and Laboratory components (if applicable). |   |       |  |
| Examinations                       | 3-hour midterm exam in Week 6.<br>3-hour final exam (Only materials covered after midterm exam will be tested).  |   |       |  |
| Other<br>Evaluation<br>Information | None   |   |       |  |
| Other<br>Information               | None   |   |       |  |

# **Course Content**

| Week | Hours | Chapters /<br>Section        | Topic, description   |
|------|-------|------------------------------|--|
| 1-2  | 6     | Distributed<br>lecture notes | Module 1: Single-Stage Amplifiers<br>- Capacitances of MOSFETs |

|     |   |                              | - Miller theorem<br>- Common-source, common-gate, & common-drain amplifiers<br>- Cascade amplifiers<br>- Current mirrors   |
|-----|---|------------------------------|--|
| 3-4 | 6 | Distributed<br>lecture notes | Module 2: Differential Amplifiers<br>- Single-ended versus differential signaling<br>- Differential voltage gain<br>- Differential-input single-ended output amplifiers<br>- Rejection of supply and ground noise<br>- Common-mode voltage gain<br>- Slew rate<br>- Mismatch<br>- Common-mode feedback<br>- Common-mode input voltage range<br>- Cascode differential amplifiers<br>- Frequency response of amplifiers<br>- Amplifiers with negative feedback<br>- Frequency compensation in amplifiers. |
| 5   | 3 | Distributed<br>lecture notes | Module 3: Band-gap References<br>- Characterization of bandgap references<br>- Voltage biasing and current biasing<br>- Voltage and temperature sensitivities<br>- Voltage-biasing versus current-biasing<br>- Voltage-insensitive current references<br>- Temperature-insensitive voltage references<br>- Voltage/temperature-insensitive voltage references<br>- Distribution of current references<br>- Replica biasing techniques<br>- Voltage regulars  |
| 6   | 3 |                              | Midterm examination (3-hour open-book take-home examination)   |
| 7-8 | 6 | Distributed<br>lecture notes | Module 4: Phase-Locked Loops (PLLs)  - Voltage-controlled ring oscillators - Voltage-controlled LC oscillators - Noise and distortion - Spectrum (phase noise) of oscillators - Phase detectors - Charge pumps - Loop filters - Loop dynamics of type-1 PLLs - Loop dynamics of type-2 PLLs - Phase noise of PLLs - Time-to-digital converters - All-digital PLLs  |

|           |   |                              | - Frequency synthesizers   |
|-----------|---|------------------------------|--|
| 9-11      | 9 | Distributed<br>lecture notes | Module 5: Nyquist-Rate Analog-to-Digital Converters (ADCs)<br>- Fundamentals of ADCs<br>- Sampling of band-limited signals<br>- Undersampling of broadband signals<br>- Classification of ADCs<br>- Flash ADCs<br>- Pipelined ADCs<br>- Successive approximation register (SAR) ADCs<br>1) Sample-and-hold<br>2) Capacitor digital-to-analog converter (C-DAC)<br>3) SAR ADCs with bottom-plating sampling<br>4) SAR ADCs with top-plating sampling<br>5) Comparators<br>6) SAR<br>7) Switching networks |
| 12-<br>13 | 6 | Distributed<br>lecture notes | Module 6: Over-Sampled Noise-Shaping ADCs<br>- Switched capacitor networks<br>- Single-bit delta-sigma modulators<br>- Multi-bit delta-sigma modulators<br>- Noise of Nyquist ADCs<br>- Noise of oversampling delta-sigma ADCs<br>- Decimation filters   |

# Laboratory(L)/Tutorials(T)/Activity(A) Schedule

| Week                                  | L/T/A  | Description   |
|---------------------------------------|--------|---|
| Week 1 (<br>Sept. 6 -<br>10 )         | online | ADE Tutorial - Design of a common-source amplifier (5%). Report due : Mid-night of Sept. 19           |
| Week 2 (<br>Sept. 13<br>- 17)         | online | Verilog-A Tutorial - Design of analog/digital blocks (5%). Report due : Mid-night of Sept. 26         |
| Weeks<br>3-4<br>(Sept. 20<br>- Oct.1) | online | Design of a differential folded cascode voltage amplifier (10%). Report due : Mid-night of Oct.<br>10 |

| Weeks<br>5-8 (Oct.<br>4 - 29,)   | online | Design of a charge-pump phase-locked loop (10%). Report due : Mid-night of Nov. 7.               |
|----------------------------------|--------|--|
| Weeks<br>9-12<br>(Nov.1 -<br>26) | online | Design of a 4-bit successive approximation register ADC (10%). Report due : Mid-night of Dec. 5. |

### Policies & Important Information:

Students must be reminded that they are required to adhere to all relevant university policies found in their online course shell in D2L and/or on the following URL: <u>http://ryerson.ca/senate/course-outline-policies</u>

- 1. Students are required to obtain and maintain a Ryerson e-mail account for timely communications between the instructor and the students;
- 2. Any changes in the course outline, test dates, marking or evaluation will be discussed in class prior to being implemented;
- 3. Assignments, projects, reports and other deadline-bound course assessment components handed in past the due date will receive a mark of ZERO, unless otherwise stated. Marking information will be made available at the time when such course assessment components are announced.
- 4. Ryerson senate policy 157 requires that any electronic communication by students to Ryerson faculty or staff be sent from their official Ryerson email account.
- 5. Familiarize yourself with the tools you will need to use for remote learning. The <u>Continuity of Learning Guide</u> for students includes guides to completing quizzes or exams in D2L or Respondus, using D2L Brightspace, joining online meetings or lectures, and collaborating with the Google Suite.
- 6. The University has issued a minimum technology requirement for remote learning. Details can be found at: <a href="https://www.ryerson.ca/covid-19/students/minimum-technology-requirements-remote-learning">https://www.ryerson.ca/covid-19/students/minimum-technology-requirements-remote-learning</a>. Please ensure you meet the minimum technology requirements as specified in the above link.
- 7. Ryerson COVID-19 Information and Updates (available <u>https://www.ryerson.ca/covid-19/students</u>) for Students summarizes the variety of resources available to students during the pandemic.
- 8. Refer to our **Departmental FAQ** page for information on common questions and issues at the following link: <u>https://www.ee.ryerson.ca/guides/Student.Academic.FAQ.html</u>.

### **Missed Classes and/or Evaluations**

When possible, students are required to inform their instructors of any situation which arises during the semester which may have an adverse effect upon their academic performance, and must request any consideration and accommodation according to the relevant policies as far in advance as possible. Failure to do so may jeopardize any academic appeals.

- 1. Academic Consideration Requests for missed work (e.g. missing tests, labs, etc) According to <u>Ryerson Senate Policy 134</u>, sections 1.2.3, if you miss any exams, quizzes, tests, labs, and/or assignments for helth or compassionate reasons you need to inform your instructor(s) (via email whenever possible) in advance when you will be missing an exam, test or assignment deadline. When circumstances do not permit this, you must inform the instructor(s) as soon as reasonably possible "*In the case of illness, a <u>Ryerson Student Health Certificate</u>, or a letter on letterhead from an appropriate regulated health professional with the student declaration portion of the Student Health Certificate attached. For reasons other than illness, proper documentation is also required (e.g. death certificate, police report, TTC report). ALL supporting documentation for illness or compassionate grounds MUST be submitted within three (3) working days of the missed work." <u>NOTE: You are required to submit all of your pertinent documentation through Ryerson's online Academic Consideration Request system at the following link: prod.apps.ccs.ryerson.ca/senateapps/acadconsform.*</u>
- 2. *Religious, Aboriginal and Spiritual observance* If a student needs accommodation because of religious, Aboriginal or spiritual observance, they must submit a Request for Accommodation of Student Religious, Aboriginal and Spiritual

Observance AND an Academic Consideration Request form within the first 2 weeks of the class or, for a final examination, within 2 weeks of the posting of the examination schedule. If the requested absence occurs within the first 2 weeks of classes, or the dates are not known well in advance as they are linked to other conditions, these forms should be submitted with as much lead time as possible in advance of the absence. Both documents are available at <a href="http://www.ryerson.ca/senate/forms/relobservforminstr.pdf">www.ryerson.ca/senate/forms/relobservforminstr.pdf</a>. If you are a full-time or part-time degree student, then you submit the forms to your own program department or school;

 Academic Accommodation Support - Before the first graded work is due, students registered with the <u>Academic</u> <u>Accommodation Support office</u> (AAS - www.ryerson.ca/studentlearningsupport/academic-accommodation-support) should provide their instructors with an Academic Accommodation letter that describes their academic accommodation plan.

# Virtual Proctoring Information (if used in this course)

Online exam(s) within this course may use a virtual proctoring system. Please note that your completion of any such virtually proctored exam may be recorded via the virtual platform and subsequently reviewed by your instructor. The virtual proctoring system provides recording of flags where possible indications of suspicious behaviour are identified only. Recordings will be held for a limited period of time in order to ensure academic integrity is maintained and then will be deleted.

Access to a computer that can support remote recording is your responsibility as a student. The computer should have the latest operating system, at a minimum Windows (10, 8, 7) or Mac (OS X 10.10 or higher) and web browser Google Chrome or Mozilla Firefox. You will need to ensure that you can complete the exam using a reliable computer with a webcam and microphone available, as well as a typical high-speed internet connection. Please note that you will be required to show your Ryerson OneCard prior to beginning to write the exam. In cases where you do not have a Ryerson OneCard, government issued ID is permitted.

Information will be provided prior to the exam date by your instructor who may provide an opportunity to test your set-up or provide additional information about online proctoring. Since videos of you and your environment will be recorded while writing the exam, please consider preparing the background (room / walls) so that personal details are not visible, or move to a room that you are comfortable showing on camera.

### Turnitin (if used in this course)

Turnitin.com is a plagiarism prevention and detection service to which Ryerson subscribes. It is a tool to assist instructors in determining the similarity between students' work and the work of other students who have submitted papers to the site (at any university), internet sources, and a wide range of books, journals and other publications. While it does not contain all possible sources, it gives instructors some assurance that students' work is their own. No decisions are made by the service; it generates an "originality report," which instructors must evaluate to judge if something is plagiarized.

Students agree by taking this course that their written work will be subject to submission for textual similarity review to Turnitin.com. Instructors can opt to have student's papers included in the Turnitin.com database or not. Use of the Turnitin.com service is subject to the terms-of-use agreement posted on the Turnitin.com website. Students who do not want their work submitted to this plagiarism detection service must, by the end of the second week of class, consult with their instructor to make alternate arrangements.

Even when an instructor has not indicated that a plagiarism detection service will be used, or when a student has opted out of the plagiarism detection service, if the instructor has reason to suspect that an individual piece of work has been plagiarized, the instructor is permitted to submit that work in a non-identifying way to any plagiarism detection service.

# **Academic Integrity**

Ryerson's <u>Policy 60 (the Academic Integrity policy)</u> applies to all students at the University. Forms of academic misconduct include plagiarism, cheating, supplying false information to the University, and other acts. The most common form of academic misconduct is plagiarism - a serious academic offence, with potentially severe penalties and other consequences. It is expected, therefore, that all examinations and work submitted for evaluation and course credit will be the product of each student's individual effort (or an authorized group of students). Submitting the same work for credit to more than one course, without instructor approval, can also be considered a form of plagiarism.

Suspicions of academic misconduct may be referred to the Academic Integrity Office (AIO). Students who are found to have committed academic misconduct will have a Disciplinary Notation (DN) placed on their academic record (not on their transcript) and will normally be assigned one or more of the following penalties:

- 1. A grade reduction for the work, ranging up to an including a zero on the work (minimum penalty for graduate work is a zero on the work);
- 2. A grade reduction in the course greater than a zero on the work. (Note that this penalty can only be applied to course components worth 10% or less, and any additional penalty cannot exceed 10% of the final course grade. Students must be given prior notice that such a penalty will be assigned (e.g. in the course outline or on the assignment handout);
- 3. An F in the course;
- 4. More serious penalties up to and including expulsion from the University.

The unauthorized use of intellectual property of others, including your professor, for distribution, sale, or profit is expressly prohibited, in accordance with Policy 60 (Sections 2.8 and 2.10). Intellectual property includes, but is not limited to:

- 1. Slides
- 2. Lecture notes
- 3. Presentation materials used in and outside of class
- 4. Lab manuals
- 5. Course packs
- 6. Exams

For more detailed information on these issues, please refer to the <u>Academic Integrity</u> <u>policy(https://www.ryerson.ca/senate/policies/pol60.pdf)</u> and to the Academic Integrity Office website (https://www.ryerson.ca/academicintegrity/).

### Academic Accommodation Support

Ryerson University acknowledges that students have diverse learning styles and a variety of academic needs. If you have a diagnosed disability that impacts your academic experience, connect with Academic Accommodation Support (AAS). Visit the <u>AAS website</u> or contact <u>aasadmin@ryerson.ca</u> for more information.

Note: All communication with AAS is voluntary and confidential, and will not appear on your transcript.

#### Important Resources Available at Ryerson

- 1. The Library (<u>https://library.ryerson.ca/</u>) provides research workshops and individual assistance. If the University is open, there is a Research Help desk on the second floor of the library, or go to <u>https://library.ryerson.ca/workshops</u>
- 2. Student Learning Support (<u>https://www.ryerson.ca/student-life-and-learning/learning-support/</u>) offers group-based and individual help with writing, math, study skills and transition support, as well as resources and checklists to support students as online learners (<u>https://www.ryerson.ca/student-life-and-learning/learning-support/</u>).
- 3. You can submit an Academic Consideration Request (<u>https://prod.apps.ccs.ryerson.ca/senateapps/acadconsform</u>) when an extenuating circumstance has occurred that has significantly impacted your ability to fulfill and academic requirement. You may always visit the Senate website (<u>https://www.ryerson.ca/senate/</u>) and select the blue radial button on the top right hand side entitled: Academic Consideration Request (ACR) to submit the request.

Policy 167: Academic Concideration due to COVID-19: Students that miss an assessment due to cold or flu-like symptoms, or due to self isolation, are currently not required to provide a health certificate. Other absences must follow <u>Policy 167: Academic</u> <u>Consideration</u>.

Also NOTE: Outside of COVID-19 symptoms, the new Policy 167: Academic Consideration does allow for a once per term academic consideration request without supporting documentation if the absence is less than 3 days in duration and is not for a final exam/final assessment. In the absence is more than 3 days in duration and/or is for a final exam/final assessment, documentation is required. For more information please see Senate Policy 167: Academic Consideration.

- 4. Ryerson COVID-19 Information and Updates for Students (<u>https://www.ryerson.ca/covid-19/students/</u>) summarizes the variety of resources available to students during the pandemic.
- 5. Familiarize yourself with the tools you will need to use for remote learning. The Continuity of Learning Guide (<u>https://www.ryerson.ca/centre-for-excellence-in-learning-and-teaching/learning-guide/</u>) for students includes guides to completing quizzes or exams in D2L Brightspace, with or without <u>Respondus LockDown Browser and Monitor</u>, <u>using D2L</u> <u>Brightspace</u>, joining online meetings or lectures, and collaborating with the Google Suite.
- 6. Information on Copyright for Faculty (<u>https://library.ryerson.ca/copyright/faculty/copyright-faqs/my-teaching-materials-have-been-posted-online/</u> and students (<u>https://library.ryerson.ca/copyright/home/copyright-for-students-2/students-course-sharing-websites-and-file-sharing/</u>).
- 7. At Ryerson, we recognize that things can come up throughout the term that may interfere with a student's ability to succeed in their coursework. These circumstances are outside of one's control and can have a serious impact on physical and mental well-being. Seeking help can be a challenge, especially in those times of crisis. Below are resources we encourage all Ryerson community members to access to ensure support is reachable. <u>https://www.ryerson.ca/mental-health-wellbeing</u>.

#### If support is needed immediately, you can access these outside resources at anytime:

- Distress Line 24/7 line for if you are in crisis, feeling suicidal or in need of emotional support (phone: 416-408-4357)
- Good2Talk 24/7 hour line for postsecondary students (phone: 1-866-925-5454)